Consumer
Behavior (14)

Processor
Response to
Application
Workload (16)

Temperature
Frequency (20)

Temperature
Profile (25)

(28)

2765 Trequency

(35)

Figure 1: Sequential Flow of Events

Accelerated Life Model

Calculations (30)

Figure 2: Depiction of Graphical User Interface for Design Inputs

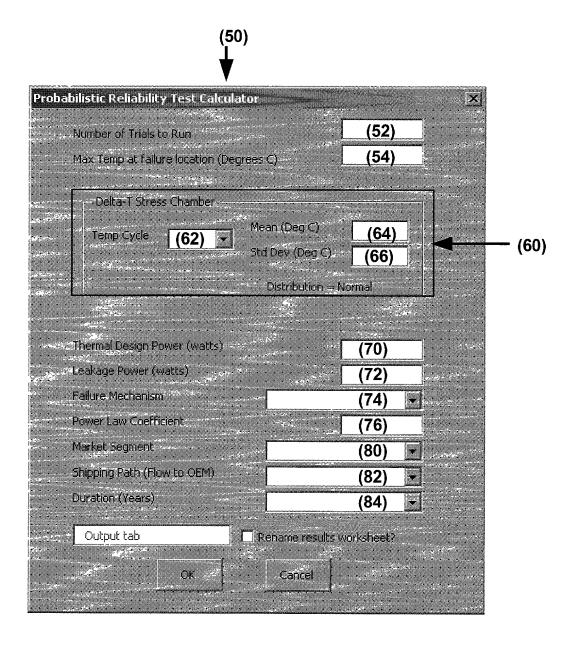


Figure 3: Representative Temperature Response

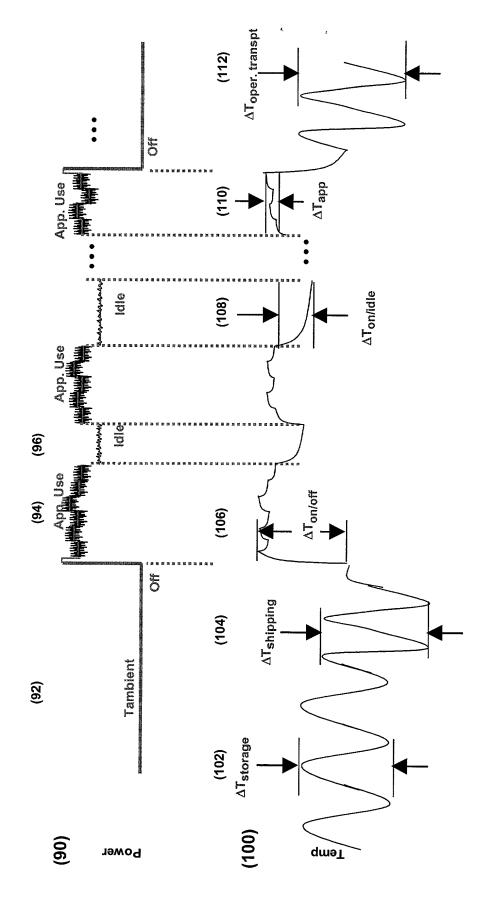


Figure 4: Modification to Coffin-Manson empirical Model

$$N_{Accel_Total} = N_{Accel_Storage} + N_{Accel_Stripping} + N_{Accel_Operating}$$

$$V_{Accel_Total} = N_{Accel_Storage} + N_{Accel_Storage} + N_{Accel_Operating} + N_{Accel_Operating}$$

$$N_{Use_On/Idle} \cdot \left(\frac{\Delta T_{Ship,air}}{\alpha_{Ship,air} \cdot \Delta T_{Accel}}\right)^n + N_{Ship,grad} \cdot \left(\frac{\Delta T_{Ship,grad}}{\alpha_{Ship,air} \cdot \Delta T_{Accel}}\right)^n + N_{Ship,grad} \cdot \left(\frac{\Delta T_{Ship,grad}}{\alpha_{Ship,air} \cdot \Delta T_{Accel}}\right)^n + N_{Use_App} \cdot \left(\frac{\Delta T_{Use_App}}{\alpha_{Use_App} \cdot \Delta T_{Accel}}\right)^n + N_{Oper_transport} \cdot \left(\frac{\Delta T_{Oper_transport}}{\alpha_{Op,_trans} \cdot \Delta T_{Accel}}\right)^n$$

$$(213) \qquad (214)$$

Figure 5: Input and output data from the Accelerated Life Testing algorithm

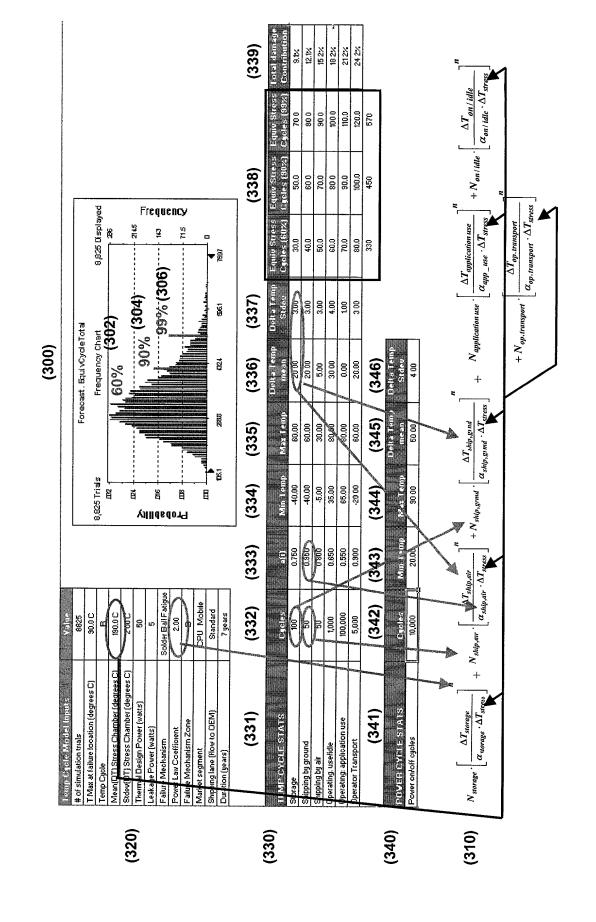


Figure 6: Illustration depicting how component location influences the size and magnitude of temperature fluctuations

